

WEST**Help Logout Interrupt****Main Menu Search Form Posting Counts Show S Numbers Edit S Numbers Preferences****Search Results -**

Terms	Documents
l43 and (storage\$ same partition\$)	2

Database:

US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Refine Search:

Search History**Today's Date: 1/4/2002**

DB Name	Query	Hit Count	Set Name
USPT	l43 and (storage\$ same partition\$)	2	<u>L44</u>
USPT	(supplier\$ same manufactur\$) and (central\$ same database\$) and component\$2	74	<u>L43</u>
USPT	(supplier\$ same manufactur\$) and component\$2	3222	<u>L42</u>
USPT	l32 and (manufactured near2 component\$2)	9	<u>L41</u>
USPT	l32 and component\$2	1108	<u>L40</u>
USPT	l37 and l32	0	<u>L39</u>
USPT	l37 and (data\$ same relat\$3)	18	<u>L38</u>
USPT	l35 and (storage\$ same partition\$)	30	<u>L37</u>
USPT	l35 and l32	0	<u>L36</u>
USPT	(develop\$ same stage\$2) and (manufactur\$3 same component\$2)	2499	<u>L35</u>
USPT	(development same stage\$2) and (manufactur\$3 same component\$2)	1089	<u>L34</u>
USPT	l28 and l32	0	<u>L33</u>
USPT	(storage\$ same partition\$) and (information\$ same relat\$3)	1594	<u>L32</u>

USPT	I28 and ((storage\$ same partition\$) same (information\$ same relat\$3))	0	<u>L31</u>
USPT	I28 and (storage\$ same (partition\$ or cell\$)) and (information\$ same relat\$3)	17	<u>L30</u>
USPT	I28 and I22	0	<u>L29</u>
USPT	(development same stage) and (manufactur\$3 same component\$1)	1076	<u>L28</u>
USPT	I22 and (manufactur\$3 and component\$ and database\$)	32	<u>L27</u>
USPT	I22 and I17	2	<u>L26</u>
USPT	I22 and I17	2	<u>L25</u>
USPT	I21 and I22	0	<u>L24</u>
USPT	I17 and I21 and I22	0	<u>L23</u>
USPT	storage\$ same location\$ same (partition\$ or cell\$) and (information\$ near2 relat\$3)	573	<u>L22</u>
USPT	(relevant near2 part\$3) same (data\$ or field\$) same database\$	69	<u>L21</u>
USPT	I18 and I19	2	<u>L20</u>
USPT	((707/\$)!..CCLS.)	11107	<u>L19</u>
USPT	I17 and (relevant near2 part\$3)	20	<u>L18</u>
USPT	manufactur\$3 and component\$ and database\$ and (development same stage\$)	848	<u>L17</u>
USPT	I12 and ((707/\$)!..CCLS.)	0	<u>L16</u>
USPT	((development near2 stage) and (relevant near2 part\$3) and database\$) and ((707/\$)!..CCLS.)	0	<u>L15</u>
USPT	(development near2 stage) and (relevant near2 part\$3) and database\$ and ((707/\$)!..CCLS.)	0	<u>L14</u>
USPT	(development near2 stage) and (relevant near2 part\$3) and database\$	7	<u>L13</u>
USPT	(development near2 stage) and (relevant near2 part\$3)	43	<u>L12</u>
USPT	(dell near2 USA) and (relevant near2 part\$3)	0	<u>L11</u>
USPT	dell and (relevant near2 part\$3)	2	<u>L10</u>
USPT	dell and database and (relevant near2 part\$3)	0	<u>L9</u>
USPT	dell and database and (development near2 stage)	2	<u>L8</u>
USPT	I1 and database and (development near2 stage)	0	<u>L7</u>
USPT	I1 and (development near2 stage)	8	<u>L6</u>
USPT	I1 and (development same stage)	14	<u>L5</u>
USPT	I1 and (manufactur\$3 same component\$) and (development same stage)	0	<u>L4</u>
USPT	I1 and (manufactur\$3 same component\$ same database)	1	<u>L3</u>
USPT	paul-j-murphy\$.in.	0	<u>L2</u>
USPT	murphy.in.	3084	<u>L1</u>

WEST**Generate Collection****Search Results - Record(s) 1 through 2 of 2 returned.**

1. Document ID: US 6327594 B1

L44: Entry 1 of 2

File: USPT

Dec 4, 2001

US-PAT-NO: 6327594

DOCUMENT-IDENTIFIER: US 6327594 B1

TITLE: Methods for shared data management in a pervasive computing environment

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)[KIMC](#) [Draw Desc](#) [Image](#)

2. Document ID: US 6289095 B1

L44: Entry 2 of 2

File: USPT

Sep 11, 2001

US-PAT-NO: 6289095

DOCUMENT-IDENTIFIER: US 6289095 B1

TITLE: NPA split management in intelligent network environment

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)[KIMC](#) [Draw Desc](#) [Image](#)**Generate Collection**

Terms	Documents
I43 and (storage\$ same partition\$)	2

[Display](#)[10](#)

Documents, starting with Document:

[2](#)Display Format: [TI](#) [Change Format](#)

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Your wildcard search against 2000 terms has yielded the results below

Search for additional matches among the next 2000 terms

starting with: DATA\$(DATALRESOURCE).P29-P90.

Search Results -

Terms	Documents
l37 and (data\$ same relat\$3)	18

Database:

- US Patents Full-Text Database
- US Pre-Grant Publication Full-Text Database
- JPO Abstracts Database
- EPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

[Refine Search:](#)[Clear](#)**Search History**

Today's Date: 1/4/2002

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT	l37 and (data\$ same relat\$3)	18	L38
USPT	l35 and (storage\$ same partition\$)	30	L37
USPT	l35 and l32	0	L36
USPT	(develop\$ same stage\$2) and (manufactur\$3 same component\$2)	2499	L35
USPT	(development same stage\$2) and (manufactur\$3 same component\$2)	1089	L34
USPT	l28 and l32	0	L33
USPT	(storage\$ same partition\$) and (information\$ same relat\$3)	1594	L32
USPT	l28 and ((storage\$ same partition\$) same (information\$ same relat\$3))	0	L31
USPT	l28 and (storage\$ same (partition\$ or cell\$)) and (information\$ same relat\$3)	17	L30

USPT	I28 and I22	0	<u>L29</u>
USPT	(development same stage) and (manufactur\$3 same component\$1)	1076	<u>L28</u>
USPT	I22 and (manufactur\$3 and component\$ and database\$)	32	<u>L27</u>
USPT	I22 and I17	2	<u>L26</u>
USPT	I22 and I17	2	<u>L25</u>
USPT	I21 and I22	0	<u>L24</u>
USPT	I17 and I21 and I22	0	<u>L23</u>
USPT	storage\$ same location\$ same (partition\$ or cell\$) and (information\$ near2 relat\$3)	573	<u>L22</u>
USPT	(relevant near2 part\$3) same (data\$ or field\$) same database\$	69	<u>L21</u>
USPT	I18 and I19	2	<u>L20</u>
USPT	((707/\$)!..CCLS.)	11107	<u>L19</u>
USPT	I17 and (relevant near2 part\$3)	20	<u>L18</u>
USPT	manufactur\$3 and component\$ and database\$ and (development same stage\$)	848	<u>L17</u>
USPT	I12 and ((707/\$)!..CCLS.)	0	<u>L16</u>
USPT	((development near2 stage) and (relevant near2 part\$3) and database\$) and ((707/\$)!..CCLS.)	0	<u>L15</u>
USPT	(development near2 stage) and (relevant near2 part\$3) and database\$ and ((707/\$)!..CCLS.)	0	<u>L14</u>
USPT	(development near2 stage) and (relevant near2 part\$3) and database\$	7	<u>L13</u>
USPT	(development near2 stage) and (relevant near2 part\$3)	43	<u>L12</u>
USPT	(dell near2 USA) and (relevant near2 part\$3)	0	<u>L11</u>
USPT	dell and (relevant near2 part\$3)	2	<u>L10</u>
USPT	dell and database and (relevant near2 part\$3)	0	<u>L9</u>
USPT	dell and database and (development near2 stage)	2	<u>L8</u>
USPT	I1 and database and (development near2 stage)	0	<u>L7</u>
USPT	I1 and (development near2 stage)	8	<u>L6</u>
USPT	I1 and (development same stage)	14	<u>L5</u>
USPT	I1 and (manufactur\$3 same component\$) and (development same stage)	0	<u>L4</u>
USPT	I1 and (manufactur\$3 same component\$ same database)	1	<u>L3</u>
USPT	paul-j-murphy\$.in.	0	<u>L2</u>
USPT	murphy.in.	3084	<u>L1</u>

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Your wildcard search against 2000 terms has yielded the results below

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Search Results - Record(s) 1 through 10 of 18 returned.

1. Document ID: US 6233702 B1

L38: Entry 1 of 18

File: USPT

May 15, 2001

US-PAT-NO: 6233702

DOCUMENT-IDENTIFIER: US 6233702 B1

TITLE: Self-checked, lock step processor pairs

[Full](#) | [Title](#) | [CIT.1](#) | [REV.1](#) | [CLS.1](#) | [REF.1](#) | [DRAW.1](#)

2. Document ID: US 6157967 A

L38: Entry 2 of 18

File: USPT

Dec 5, 2000

US-PAT-NO: 6157967

DOCUMENT-IDENTIFIER: US 6157967 A

TITLE: Method of data communication flow control in a data processing system using busy/ready commands

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [REF.2](#)

[KIMC](#) | [DRAW.2](#) | [Image](#)

3. Document ID: US 6151689 A

L38: Entry 3 of 18

File: USPT

Nov 21, 2000

US-PAT-NO: 6151689

DOCUMENT-IDENTIFIER: US 6151689 A

TITLE: Detecting and isolating errors occurring in data communication in a multiple processor system

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#)

[KIMC](#) | [Draw Desc](#) | [Image](#)

4. Document ID: US 5964835 A

L38: Entry 4 of 18

File: USPT

Oct 12, 1999

US-PAT-NO: 5964835

DOCUMENT-IDENTIFIER: US 5964835 A

TITLE: Storage access validation to data messages using partial storage address data indexed entries containing permissible address range validation for message source

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)

[KMC](#) [Drawn Desc](#) [Image](#)

5. Document ID: US 5914953 A

L38: Entry 5 of 18

File: USPT

Jun 22, 1999

US-PAT-NO: 5914953

DOCUMENT-IDENTIFIER: US 5914953 A

TITLE: Network message routing using routing table information and supplemental enable information for deadlock prevention

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)

[KMC](#) [Drawn Desc](#) [Image](#)

6. Document ID: US 5867501 A

L38: Entry 6 of 18

File: USPT

Feb 2, 1999

US-PAT-NO: 5867501

DOCUMENT-IDENTIFIER: US 5867501 A

TITLE: Encoding for communicating data and commands

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)

[KMC](#) [Drawn Desc](#) [Image](#)

7. Document ID: US 5838894 A

L38: Entry 7 of 18

File: USPT

Nov 17, 1998

US-PAT-NO: 5838894

DOCUMENT-IDENTIFIER: US 5838894 A

TITLE: Logical, fail-functional, dual central processor units formed from three processor units

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)

[KMC](#) [Drawn Desc](#) [Image](#)

8. Document ID: US 5822218 A

L38: Entry 8 of 18

File: USPT

Oct 13, 1998

US-PAT-NO: 5822218

DOCUMENT-IDENTIFIER: US 5822218 A

TITLE: Systems, methods and computer program products for prediction of defect-related failures in integrated circuits

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)[KIMC](#) [Drawn Desc](#) [Image](#) 9. Document ID: US 5790776 A

L38: Entry 9 of 18

File: USPT

Aug 4, 1998

US-PAT-NO: 5790776

DOCUMENT-IDENTIFIER: US 5790776 A

TITLE: Apparatus for detecting divergence between a pair of duplexed, synchronized processor elements

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)[KIMC](#) [Drawn Desc](#) [Image](#) 10. Document ID: US 5751955 A

L38: Entry 10 of 18

File: USPT

May 12, 1998

US-PAT-NO: 5751955

DOCUMENT-IDENTIFIER: US 5751955 A

TITLE: Method of synchronizing a pair of central processor units for duplex, lock-step operation by copying data into a corresponding locations of another memory

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)[KIMC](#) [Drawn Desc](#) [Image](#)[Generate Collection](#)

Terms	Documents
I37 and (data\$ same relat\$3)	18

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11. Document ID: US 5751932 A

L38: Entry 11 of 18

File: USPT

May 12, 1998

US-PAT-NO: 5751932

DOCUMENT-IDENTIFIER: US 5751932 A

TITLE: Fail-fast, fail-functional, fault-tolerant multiprocessor system

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#)

[KIMC](#) | [Drawn Desc](#) | [Image](#)

12. Document ID: US 5689689 A

L38: Entry 12 of 18

File: USPT

Nov 18, 1997

US-PAT-NO: 5689689

DOCUMENT-IDENTIFIER: US 5689689 A

TITLE: Clock circuits for synchronized processor systems having clock generator circuit with a voltage control oscillator producing a clock signal synchronous with a master clock signal

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#)

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13. Document ID: US 5675807 A

L38: Entry 13 of 18

File: USPT

Oct 7, 1997

US-PAT-NO: 5675807

DOCUMENT-IDENTIFIER: US 5675807 A

TITLE: Interrupt message delivery identified by storage location of received interrupt data

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#)

[KIMC](#) | [Drawn Desc](#) | [Image](#)

14. Document ID: US 5675579 A

L38: Entry 14 of 18

File: USPT

Oct 7, 1997

US-PAT-NO: 5675579

DOCUMENT-IDENTIFIER: US 5675579 A

TITLE: Method for verifying responses to messages using a barrier message

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)

[KIMC](#) [Drawn Desc](#) [Image](#)

15. Document ID: US 5574849 A

L38: Entry 15 of 18

File: USPT

Nov 12, 1996

US-PAT-NO: 5574849

DOCUMENT-IDENTIFIER: US 5574849 A

TITLE: Synchronized data transmission between elements of a processing system

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)

[KIMC](#) [Drawn Desc](#) [Image](#)

16. Document ID: US 4686365 A

L38: Entry 16 of 18

File: USPT

Aug 11, 1987

US-PAT-NO: 4686365

DOCUMENT-IDENTIFIER: US 4686365 A

TITLE: Fourier transform ion cyclothon resonance mass spectrometer with spatially separated sources and detector

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)

[KIMC](#) [Drawn Desc](#) [Image](#)

17. Document ID: US 4293919 A

L38: Entry 17 of 18

File: USPT

Oct 6, 1981

US-PAT-NO: 4293919

DOCUMENT-IDENTIFIER: US 4293919 A

TITLE: Level sensitive scan design (LSSD) system

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)

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18. Document ID: US 4051352 A

L38: Entry 18 of 18

File: USPT

Sep 27, 1977

US-PAT-NO: 4051352

DOCUMENT-IDENTIFIER: US 4051352 A

TITLE: Level sensitive embedded array logic system

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)

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Terms	Documents
I37 and (data\$ same relat\$3)	18

Documents, starting with Document:

Display Format: